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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki

Art Unit :

Serial No.: 09/774,388

Examiner:

Thien F.

Filed

: January 30, 2001

Confirmation No.: 3058

Title

: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD

THEREOF

Commissioner for Patents Washington, D.C. 20231

## AMENDMENT

In response to the Official Action dated October 21, Paper No. 10, in the above-referenced application, kindly the above-identified application as follows.

## In the Abstract:

Please replace the Abstract of the Disclosure with the following rewritten Abstract as follows:

A high performance electric device which uses an adhesive layer over a substrate. A color filter is over a substrate, and an adhesive layer is also located over the substrate and color An insulating layer is over the adhesive layer, and thin film transistors cover the insulating film and the color filters. Light emitting elements cover the thin film

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

01/29/2003 GGEBREGI 00000046 09774388

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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